

Appl. No. : 09/196,658
Filed : November 19, 1998

REMARKS

In response to the final Office Action mailed May 4, 2004, Applicant has amended the application as above. No new matter is added by the amendments as discussed below. Applicant respectfully requests the entry of the amendments and reconsideration of the application in view of the amendments and the remarks set forth below.

Discussion of Drawing Amendments

Pursuant to the Examiner's suggestions, Figure 6 has been amended to show every feature of the claimed invention. The amendments to Figure 6 (regarding L1 and L2 demodulators) are supported, for example, by original Claim 8. Furthermore, the amendments to Figure 6 (regarding L1 and L2 demodulators) are supported, for example, by the specification at page 13, lines 30-31, page 17, lines 1-2, and page 19, lines 9-11. The amendments to Figures 5 and 6 (regarding IQ0-IQ7) are merely for clarification and supported, for example, by the specification at page 16, lines 26-27 and Figure 4. Thus, no new matter has been added to the drawings.

Discussion of Specification Amendments

Pursuant to the Examiner's suggestions, the specification has been amended to provide proper antecedent basis for the claimed subject matter. That is, the terms "the correlator units 317 and 327" have been amended to "the correlator unit (L1 demodulator) 317" and "the correlator unit (L2 demodulator) 327," respectively. Since the specification has been amended to merely reflect the changes of Figure 6, no new matter has been added to the specification.

Discussion of Claim Amendments

Claims 8 and 16-19 have been amended. Upon the entry of the amendments, Claims 8 and 16-19 are pending in this application. The amendments to Claims 17-19 are merely to conform them to U.S. practice, and do not narrow the scope of protection. The amendments to Claims 8 and 16 are supported, for example, by the original disclosure as discussed below.

1. With regard to the term “each L1 and L2 signal being converted into a plurality of digital signals”

This term is supported by embodiments disclosed in the description, for example, the specification at page 5, lines 26-28 and page 12, lines 28-30. That is, the received L1 and L2 signals are converted into a plurality of digital signals, e.g., IQ0-IQ7 (see, for example, the specification at page 16, lines 26-27). In one embodiment, the signals IQ0-IQ7 are the bussed signal representation of both L1 and L2 signals (see also the specification at page 10, lines 12-16). Thus, in one embodiment, each of IQ0-IQ7 is associated with both L1 and L2 signals.

2. With regard to the term “selectably switching the converted L1 and L2 signals”

This term is supported by embodiments disclosed in the description, for example, the specification at page 17, lines 4-5 and page 19, lines 9-11. Referring to the above-indicated portions, the input selector (302) of Figure 4 selects one of IQ0-IQ7 and each of the input selector switches shown in Figure 6 has the same function as the input selector (302).

3. With regard to the term “demodulating the converted L1 and L2 signals”

This term is supported, for example, by original Claims 8 and 16. Furthermore, the term is supported by embodiments disclosed in the description, for example, the specification at page 17, lines 1-2 and lines 4-5, and page 19, lines 9-11, and Figure 6. As discussed above, since each of IQ0-IQ7 is associated with both L1 and L2 signals, each demodulator demodulates the converted L1 and L2 signals.

4. With regard to the term “the first (second) demodulator output being associated with the L1 (L2) signal”

This term is supported by, for example, the specification at page 13, lines 30-31, page 17, lines 1-2, page 19, lines 9-11, and Figure 6. Referring to the above-indicated portions, it can be seen that the outputs of the L1 and L2 demodulators (e.g., 317, 327) are associated with L1 and L2 signals, respectively.

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5. Summary

As discussed above, all of the claim terms are supported by the original disclosure. Therefore, the claim amendments do not introduce any new matter.

Amendments After Final Rejection

The amendments to the drawing, specification and the claims are merely to adopt the Examiner's suggestions pointed out in the Ex parte Quayle Action, or in some other way require only a cursory review by the Examiner. MPEP 714.13. Furthermore, the amendments neither raise new issues nor require new prior art search. Thus, entry of the amendments is respectfully requested.

Discussion of Drawing Objections

The drawings have been objected to under 37 CFR § 1.83(a). The Examiner notes that the drawings must show every feature of the claimed invention. Applicant respectfully submits that the drawings show all of the features of the claimed invention as discussed below.

1. The terms "taps included in the delay line"

In one embodiment of the invention, Figure 8 shows that the delay line (52) includes a plurality of taps (0-12). The specification also describes this term at page 22, lines 13-18.

2. The first demodulator for demodulating the converted L1 and L2 signals

Figure 6 (see channel 31) shows in connection with one embodiment of the invention that the first demodulator (317) demodulates the converted L1 and L2 signals (one of IQ0-IQ7 which has been selected, each IQ signal being associated with both L1 and L2 signals).

3. The second demodulator selectably connectable to any one of the taps of the delay line for demodulating the converted L1 and L2 signals

Figure 6 (see channel 32) shows in connection with one embodiment of the invention that the second demodulator (e.g., 327) demodulates the converted L1 and L2 signals (a

selected one of IQ0-IQ7). Furthermore, Figures 6 and 8 show that the second demodulator (327) is selectably connectable to any one of the taps of the delay line (of channel 32 of Figure 6). Referring to the specification at page 21, lines 10-11, Figure 8 shows a code delay line (308) according to one embodiment of the invention. That is, the code delay line (of channel 32 of Figure 6) has the same configuration as that of Figure 8.

4. The switches for selectably switching the converted L1 and L2 signals for demodulation by the second demodulator, and selectably switching the converted L1 and L2 signals for demodulation by the first demodulator

Figure 6 shows in connection with one embodiment of the invention that the input selector switch (next to element "315" of Figure 6) selectably switches the converted L1 and L2 signals (e.g., one of IQ0-IQ7) for demodulation by the second demodulator (e.g., 327), and the input selector switch (next to element "325" of Figure 6) selectably switches the converted L1 and L2 signals (e.g., another of IQ0-IQ7) for demodulation by the first demodulator (e.g., 317).

5. Summary

As discussed above, all of the features of Claim 8 are shown on the drawings. Claim 16 includes a similar limitation. Thus, the drawings represent embodiments which show every feature of the claimed invention. Withdrawal of the drawing objections is respectfully requested.

Discussion of Specification Objections

The specification has been objected to as failing to provide antecedent basis for the claimed subject matter. The Examiner notes that the specification needs to be amended to provide support for the claim limitations. Applicant respectfully submits that the specification provides sufficient support for the claim terms as discussed below.

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1. The first demodulator for demodulating the converted L1 and L2 signals

As discussed above, the specification provides antecedent basis for this term, for example, at page 17, lines 1-2 and lines 4-5, and page 19, lines 9-11.

2. The second demodulator selectably connectable to any one of the taps of the delay line for demodulating the converted L1 and L2 signals

As discussed above, the specification provides antecedent basis for this term, for example, at page 17, lines 1-2 and lines 4-5, page 19, lines 9-11, and page 21, lines 10-11.

3. The switches for selectably switching the converted L1 and L2 signals for demodulation by the second demodulator, and selectably switching the converted L1 and L2 signals for demodulation by the first demodulator

As discussed with regard to the claim amendments, this term is supported by, for example, at page 17, lines 4-5, and page 19, lines 9-11.

4. Summary

As discussed above, all of the features of Claim 8 are supported by the specification. Claim 16 includes a similar limitation. Thus, the specification provides proper antecedent basis for the claimed subject matter. Withdrawal of the specification objections is respectfully requested.

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CONCLUSION

In view of Applicant's amendments to the application, and the foregoing remarks, it is respectfully submitted that the present application is in condition for allowance. Should the Examiner have any remaining concerns which might prevent the prompt allowance of the application, the Examiner is respectfully invited to contact the undersigned at the telephone number appearing below.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: _____

9/3/04

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